# SSM 2040

# **VOLTAGE CONTROLLED FILTER CIRCUIT**

#### DESCRIPTION

The SSM 2040\*is a four section filter whose cutoff frequency can be exponentially voltage controlled over a 10,000 to 1 range. This flexible building-block can be used in virtually any active filter design including lowpass, highpass, bandpass and notch. Rolloff characteristics can be selected to be Butterworth, Bessel, Chevyshev, Cauer or any other filter type.

Applications include tracking filters, organs, music synthesizers, music phase shifters and sound effects generation.

#### **FEATURES**

- +/- 15V Supplies
- Exponential Frequency Control Response
- 4 Filter Sections in One Package
- Low Noise
- Low Distortion
- Guaranteed Control Rejection Characteristics
- 10,000:1 Sweep Range

#### **APPLICATIONS**

■ Voltage Controlled Filters:

Lowpass

**Biquad** 

**Bandpass** 

State Variable

Highpass

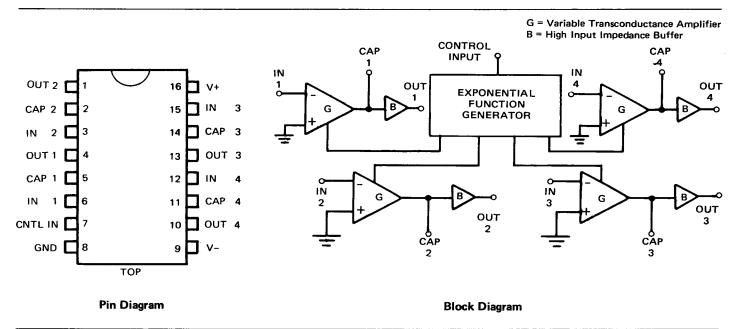
Sallen & Key

**Allpass** 

Cauer

Notch

- Parametric Equalizers
- Music Synthesizers
- Music Phase Shifters
- Tracking Filters
- Low Distortion Sine VCO's



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<sup>\*</sup>Covered by U.S. Patent Number 3,969,682.

#### **SPECIFICATIONS:**

$$V_S = +/-15V$$
,  $T_A = 25^{\circ}C$ 

SPECIFICATION	CONDITIONS	_ MIN	TYP	MAX	UNIT
Functional Range		10,000:1			
Input Offset, each cell			2	5	mV
ΔInput Offset, 4 cells in series	Vcntl= 0 mV, -90 mV Vcntl= 0 mV, +90 mV		0.6 0.6	3 3	mV mV
Transconductance	Vcntl= 0	1/10K	1/5K	1/3K	mhos
Equiv. Input Noise, each cell	20 Hz-20 KHz, Vcntl=-90 mV		0.5		uV RMS
Distortion (THD), Ein=30 mVpp	F=1 KHz, Vcntl=-90 mV		0.1		%
Tempco of Transconductance	Vcntl=0		+0.5		%/°C
Control Sensitivity			-18		mV/oct
Tempco of Control Sensitivity			0.33		%/°C
Power Supply Current	VcntI=0	2	4	7	mA
Buffer Slew Rate			2	1	V/usec
Buffer Output Sink Current		425	560	750	μΑ

## **ABSOLUTE MAXIMUM RATINGS:**

Any Pin to V
Current at any pin

Operating Temperature

Storage Temperature

Power Dissipation

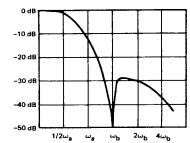
-36V

20mA

0-70° C

-55-125° C

625 mW



CAUER FILTER RESPONSE  $-\omega_b = 2\omega_a$ 

#### **APPLICATIONS FIGURES:**

Filter Type	Figure	Gain (K)	$\omega_{{f o}}$	d=1/Q	XFER char. (S=jω/ω <sub>0</sub> )
Lowpass — Real Pole	1	-R2/R1	200G/R2C	2	-K/(S+1)
Highpass — Real Pole	2	1	200G/RC	2	S/(S+1)
Allpass	3	1	200G/RC	2	(S-1)/(S+1)
Sallen & Key Lowpass	4	R2/R1	200G/R2C	2-(R4/R3)	K/(S <sup>2</sup> +dS+1)
Sallen & Key Highpass	5	1	200G/R1C	2-(R3/R2)	S <sup>2</sup> /(S <sup>2</sup> +dS+1)
Sallen & Key Bandpass	6	-R2/R1	200G/R2C	2-(R2/R3)	-KS/(S <sup>2</sup> +dS+1)
State Variable LP	7	-R2/R1	200G/R3C	*	-K/(S <sup>2</sup> +dS+1)
State Variable BP	7	R2/R1	200G/R3C	*	KS/(S <sup>2</sup> +dS+1)
State Variable HP	7	-R2/R1	200G/R3C	*	$-KS^2/(S^2+dS+1)$
Cauer (Elliptical)	8	R2/R1	ω <sub>a</sub> =200G/C ω <sub>b</sub> =200G/C		$K(Sb^2+1)/((Sa+1)^2 (Sb+1)^2)$

<sup>\*=</sup> R5 (2R1+R2)/(R1(R4+R5) ); R4 parallel R5=R1 parallel R2/2



B = High Input Impedance Buffer.

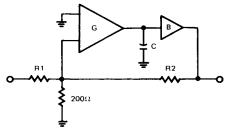


Figure 1-Lowpass Real Pole

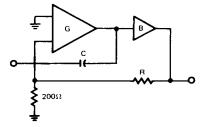


Figure 2-Highpass Real Pole

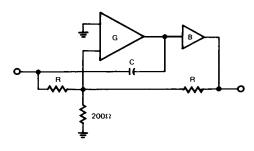


Figure 3—Allpass (Phase-shift)

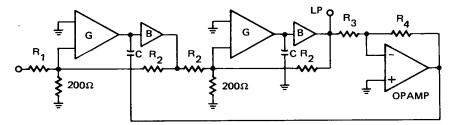


Figure 4—Sallen & Key Lowpass

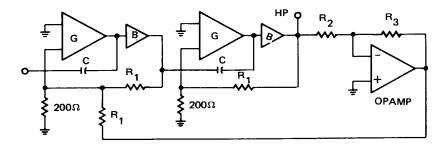


Figure 5-Sallen & Key Highpass

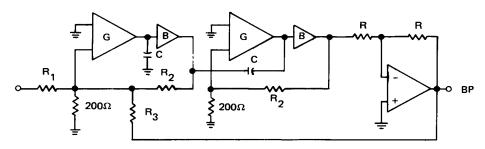


Figure 6-Sallen & Key Bandpass

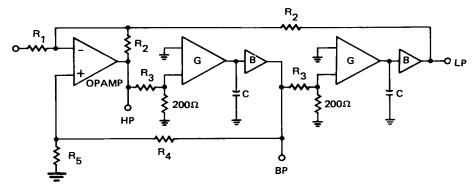


Figure 7-State Variable Filter

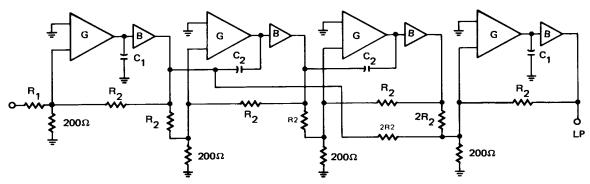


Figure 8-Cauer (Elliptical) Filter

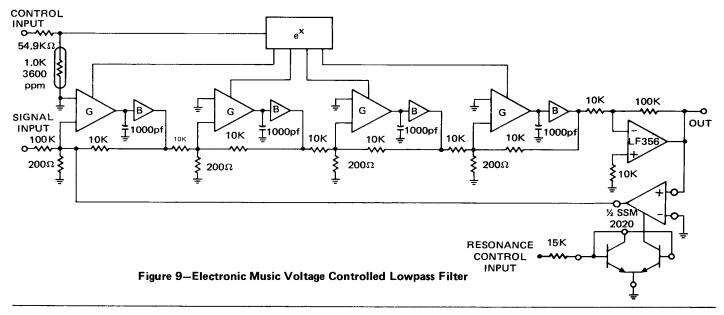
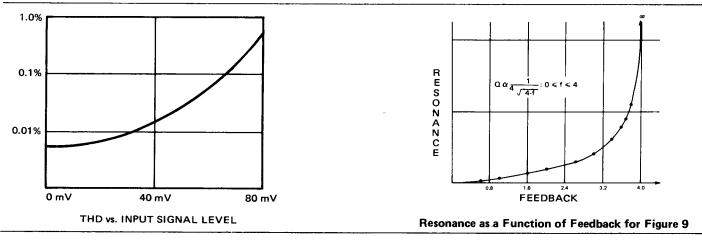


Figure 9 is a voltage controlled lowpass filter with voltage controlled resonance for electronic music applications. The frequency control input sensitivity is 1 volt/octave, temperature compensated. At high resonance settings the filter will oscillate with a pure sine wave. The output signal level is 10V p/p max.



### **DESIGN HINTS:**

- 1. The output pins (OUT1-OUT4) are only capable of swinging +/- 1V, and sinking 500 μA DC. Hence a 10K feedback resistor & load will give good performance.
- 2. Control input sensitivity tempco is best cancelled by a +3300 ppm resistor (Tel Labs Q81 or equiv.) as shown in Figure 9.
- 3. C values should be kept above 1000 pF to insure stability at all control settings.
- 4. The 200 ohm attenuating resistor is chosen for optimal control rejection. Other values can be used with some degradation of this parameter.
- 5. The outputs are not short-circuit protected. Care should be taken to avoid shorting any OUT, CAP or IN pin to either supply.



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