

CEM 3312

Voltage Controlled Envelope Generator

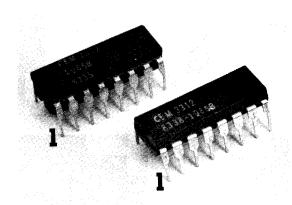
The CEM 3312 is a self-contained, precision ADSR type envelope generator intended for musical instruments and other sound generation applications. A unique feature of this device is an additional control input, called Peak, which allows the overall amplitude of the envelope waveform to be varied without affecting any of the time constants. Thus, "amount of envelope" can be precisely controlled and without the need for an extra VCA. The Peak control input may be varied from 0 to +6.5 volts, causing the envelope amplitude to vary from 0 to +5 volts.

Attack, decay, and release times are exponentially voltage controllable over a wide range, and the sustain level is linearly voltage controllable from 0 to 100% of the peak voltage. The novel design approach pioneered by Curtis still provides the best control voltage rejection in the industry. In addition, much care has been given in the design to allow superior tracking and repeatability of all parameters from unit to unit without trimming (such as using an external resistor for Rx instead of the wide tolerance monolithic resistor used in competing units). Finally, the control voltage required for all five inputs ranges from zero to positive, allowing easy interface to the system Sample & Holds.

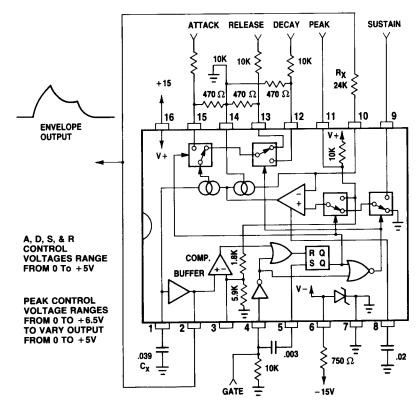
Precise, flexible, and low in cost, the CEM 3312 is the ideal choice for dynamic polyphonic envelope generation.

Features

- New Parameter, called Peak, for Controlling the Amplitude of the ADSR Waveform
- Eliminates "Envelope Amount" VCA
- True RC Envelope Shape
- All Control Inputs Range Zero to a Positive Voltage for Easy Interface to Sample & Holds.
- Exceptionally Low Control Voltage Feedthrough.
- Accurate Exponential Time Control Scales
- Good Repeatability and Tracking Between Units Without External Trim
- Independent Gate and Trigger



Circuit Block and Connection Diagram



CEM 3312

Electrical Characteristics

$V_{CC} = +15.0V$ $V_{EE} = -15.0V$ $R_X = 24K$ $T_A = 20^{\circ}C$				
Parameter	Minimum	Typical	Maximum	Units
Time Control Range	50,000:1	250,000:1		_
Attack Asymptote Voltage (V _Z) ¹ Attack Peak Voltage(V _P) ¹	6.1 4.7	6.5 5.0	6.9 5.3	V V
Peak C.V. Range Envelope Amplitude to Peak C.V. Ratio Peak C.V. Input Impedance Sustain C.V. Range ²	0 .735 3.2 0	.765 4.3 —	6.5 .795 5.4 +5.0	V Kohm V
Time Constants C.V. Range ³ Control Scale Factor Temperature Coefficient of Control Scale ATK,DCY,RLS Scale Tracking	0 +58.5 +3,000 -300	+60 +3,300 0	+240 +61.5 +3,600 +300	mV mV/Decade ppm μV/Decade
Exponential Full Scale Control Accuracy 4 50nA $<$ I $_0$ $<$ 50 μ A 2nA $<$ I $_0$ $<$ 200 μ A		0.3 2	1.5 10	% %
Attack C.V. Feedthrough ⁵ Decay C.V. Feedthrough ⁵ Release C.V. Feedthrough ⁵	_	6 NONE NONE	90	μ۷
Attack Asymptote Error (V _O - V _{CP}) Sustain Final Voltage Error (V _O - V _{CS}) Release Final Voltage Error (V _O)	-3 -3 -3	+10 +10 +10	+ 23 + 23 + 23	mV mV mV
RC Curve Asymptote Error ⁶ VCA,D,R = 0 VCA,D,R = +240mV		-6, -125	-60 -1250	μV mV
$\begin{aligned} & \text{Input Current (I_{IN}) to Output Current (I_{O})} \\ & \text{Ratio, V}_{\text{CA},D,R} = 0 \\ & \text{Charge Current (ATK)} \\ & \text{Discharge Current (DCY,RLS)} \end{aligned}$.75 .83	1 1	1.3 1.2	
Buffer Input Current (I _{B2}) Op Amp Input Current (I _{B1})	 - 150	-0.5 -400	-5 -800	nA nA
Gate Threshold Gate Input Current Trigger Pulse Required to Trigger Envelope Trigger Input Impedance	2.0 +5 +1.1 2.4	2.3 + 25 + 1.3 3	2.6 +100 +1.5 4	V μA V Kohm
Time Control Input Current Sustain Control Input Current	- 0.5 - 150	 -400	- 2500 - 800	nA nA
Output Current Sink Capability Buffer Output Impedance	- 420 100	- 560 200	- 700 350	μA ohm
Positive Supply Voltage Range Negative Supply Voltage Range ⁸ Supply Current	+10.5 4.5 5.6	 7.5	+ 18 18 9.4	V V mA

Notes

Note 1: Peak C.V. input pin

Note 2: Must never be greater than Peak C.V.

Note 3: Nominal for 10,000:1 time constant range. Can be larger. See Hints.

Note 4: Scale factor determined at mid-range. Spec represents total deviation from ideal at range extremities.

Note 5: Output is at either sustain final voltage or release final voltage. $V_{CA,D\cdot R}$ varies 0 to +240mV.

Note 6: Spec represents the difference between the actual final voltages (attack asymptote voltage, sustain final voltage, and release final voltage in the case of attack, decay, and release respectively) and the apparent voltage to which the output seems to be approaching asymptotically.

Note 7: Spec also represents time constant variation between units for VCA·D·R = 0.

Note 8: Current limiting resistor required when $V_{ee} > -6.0$ volts.

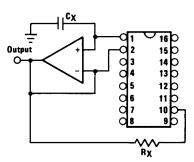
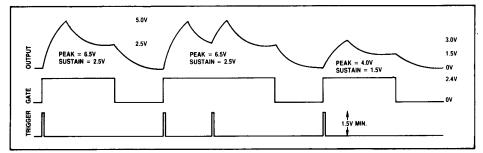


Figure 1: Connection to External Buffer

Input and Output Waveforms



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