

CEM 3371

μP Controllable Waveform Generator

The CEM 3371 is a general purpose dual voltage controlled waveform generator intended for generating arbitrary waveforms when used in conjunction with a microprocessor - DAC system. A typical application would be in the generation of envelope waveforms in musical instruments, where with only three sampled voltages, two completely independent envelopes may be generated with any starting voltage, ending voltage, shape, duration, and amplitude.

The three determining parameters are Asymptote voltage (A), Time control voltage (T), and Peak Clamp voltage (P). All three inputs are very high impedance, low bias current inputs, and therefore may be supplied from a single multiplexed DAC output with only a CMOS multiplexer and hold capacitors.

The output selected with Output Select (OS) will follow any change in the asymptote voltage level, but at a rate determined by the Time control voltage. With proper interconnection of a few external components, the output wave shape may be RC (exponential), linear, or anything in between. The Time control input varies the time constant in an exponential manner over a minimum range of 50,000:1. The Peak Clamp voltage will cause the output, as it is charging or discharging toward its asymptote, to stop and hold indefinately at that level. Whenever this occurs, the Peak

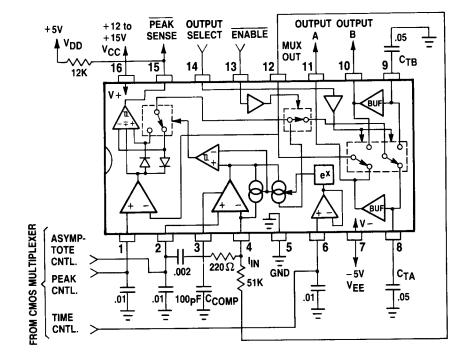
Sense output (PS) will go low, indicating to the processor that the output is in the clamped condition. The typical application for this clamp circuit would be to set the envelope peak and to indicate the end of the attack portion of the envelope.

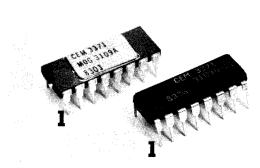
The asymptote, time, and peak clamp circuitry is switchable between two buffered outputs with the Ouput Select, and therefore may be multiplexed to generate two independent waveforms.



- Low Cost Minimal Hardware/Minimal Software System
- Extreme Flexibility Determined with Only Three Parameters
- Arbitrary Waveforms PossibleNot Limited to ADSR
- Normal and Inverted Envelope Possible
- Variable Envelope Amplitudes
- Ability to Set Waveform Quiescent Level Eliminates Initial Level S & H and C.V. Mixer.
- 0 to +5V, High Z Control Inputs for Easy Interface to System DAC through CMOS Multiplexer
- Low Offset Between Asymptote and Peak C.V. Inputs and Final Output

Block and Connection Diagram



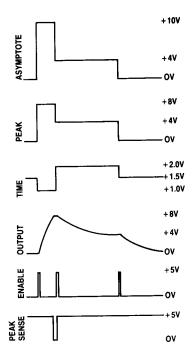


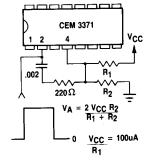
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Electrical Characteristics

V _{CC} = +15V V	_{EE} = -15V T	A = 20°C		
Parameters	Minimum	Typical	Maximum	Units
Asymptote Control Input Voltage Offset Between Asymptote	-1V	_	+14	٧
and Final Output Level	-20		+20	mV
Peak Control Input Voltage Offset Between Peak and	-2	_	+12	V
Output in Clamped State	-20		+ 20	mV
Time Control Input Voltage ¹	-2	_	+6	V
Time Constant Range	50,000:1 +1.15	+ 1.25	+1.35	V/decade
Time Control Scale Factor Time Control Scale Error ²	- -	0.3	1.0	%
Time Constant at V _{TIME} = 0 ³	0.4	0.5	0.6	R _F xC
Time Constant Tracking Between Outputs Time Constant Tracking	_	_	C1:C2	match
Between Devices		15	_	%
Output Shift with Time C.V. ⁴ Output = Asymptote (unclamped) Output = Peak (clamped) Crosstalk Between Outputs ⁵	_	1 NONE	3	mV
Output = Asymptote (unclamped) Output = Peak (clamped)		20 20	30 30	mV mV
Output Select Threshold Voltage Output Select Input Bias Current ⁶	1.7 -5	2.0 - 15	2.3 -30	V μΑ
Enable Threshold Voltage Enable Input Bias Current ⁶	2.4 -3	2.7 10	3.0 -30	ν μΑ
Peak Sense High Output Current ⁷ Peak Sense Low Output Current ⁸	_ 	_	-0.05 -0.6	mA mA
Asymptote Control Input Bias Current		±3	±10	nA
Peak Control Input Bias Current	_	± 1.5	±5	nA
Time Control Input Bias Current		±6	±20	nA
Input Bias Required by Feedback Pin(pin 4)	-2	- 15	-50	nA
Allowable Feedback Current into Feedback Pin ⁹		_	150	μА
Output Sink Current Output Source Current	-0.3 	-0.4 -	-0.5 3	mA mA
Positive Supply Range	+11.4		+16	V
Positive Supply Current	6.5	8.4	10.5	mA
Negative Supply Range Negative Supply Current	-4.5 4.5	5.6	-6 7.0	V mA

Timing





CONNECTION FOR LINEAR WAVEFORM SHAPE

Notes

Note 1. Nominal input for 10,000:1 time constant range is 0 to +5V.

Note 2. For Time control voltages between +1.5 and +5V. Maximum value increases to 10% at 0V.

Note 3. For 50% duty cycle Output Select signal. Values are halved when only one output is continuously selected.

Note 4. Time control voltage changes from 0 to +5V.

Note 5. Waveform amplitude of other channel output is 5V.P.P.

Note 6. Logic input level is +5V.

Note 7. Pull-up resistor required to digital supply not greater than +6.0V.

Note 8. $V_{LOW}=OV$. Whatever this pin drives must be capable of being pulled down to 0.1V with 400 μ A.

Note 9. For minimum time control voltage = 0V. This value will be less for time control voltages < 0V.

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